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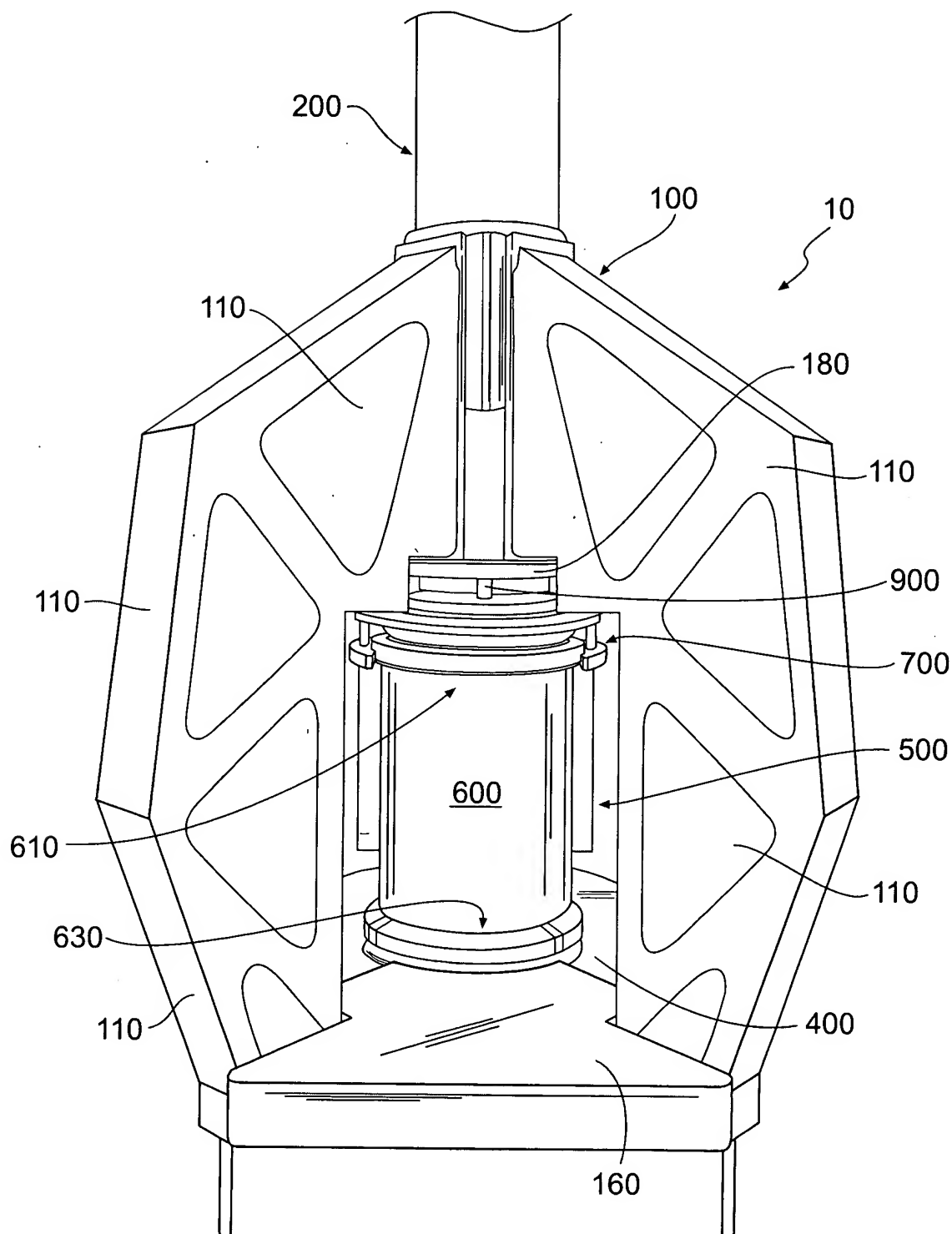
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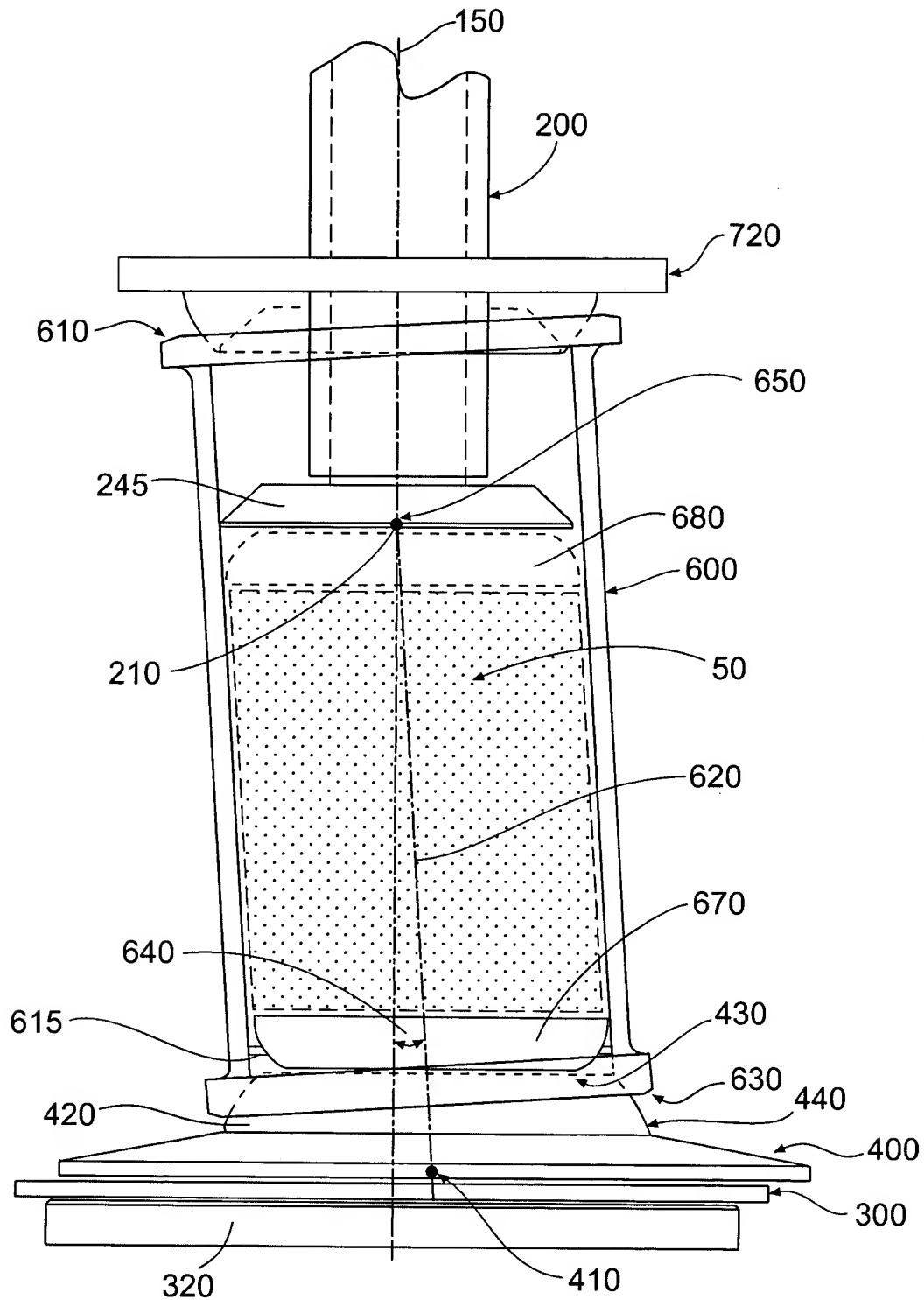


FIG. 2

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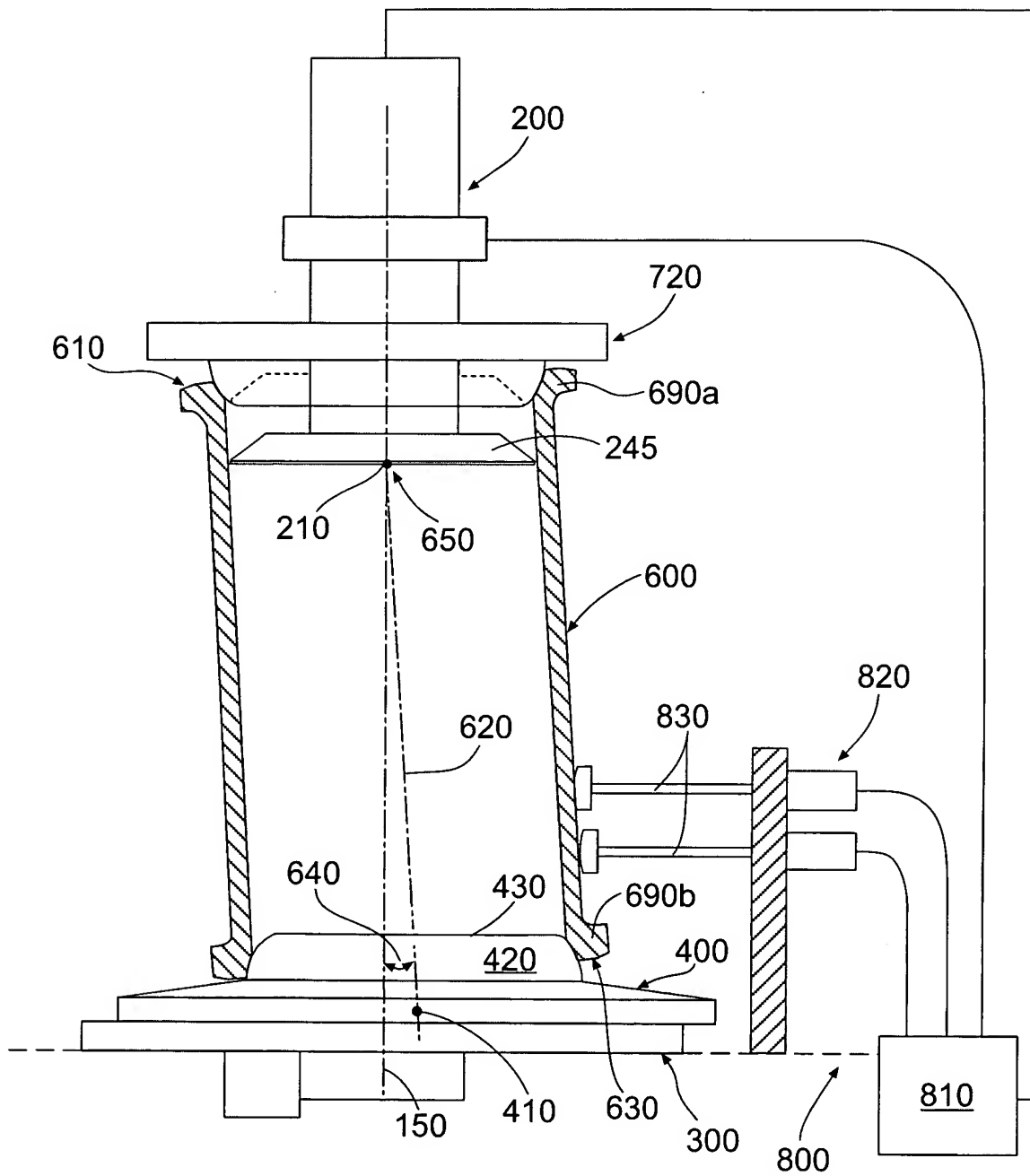


FIG. 3

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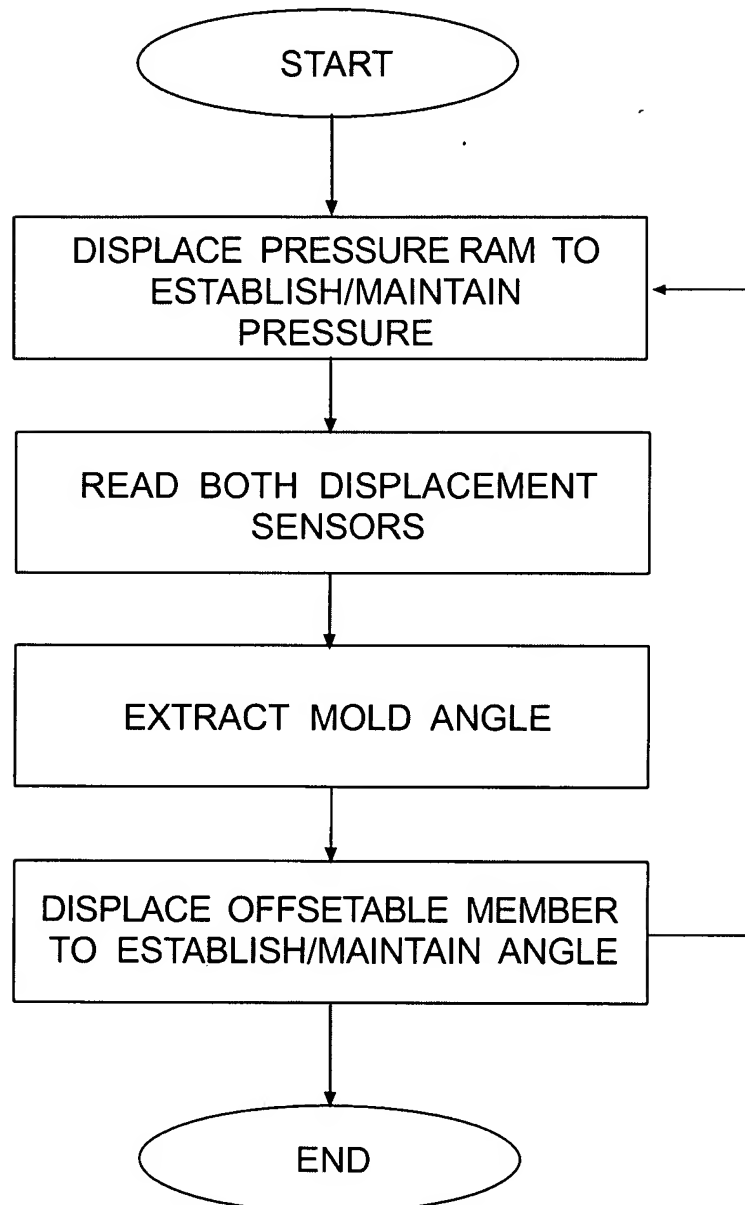


FIG. 4

FIG. 5

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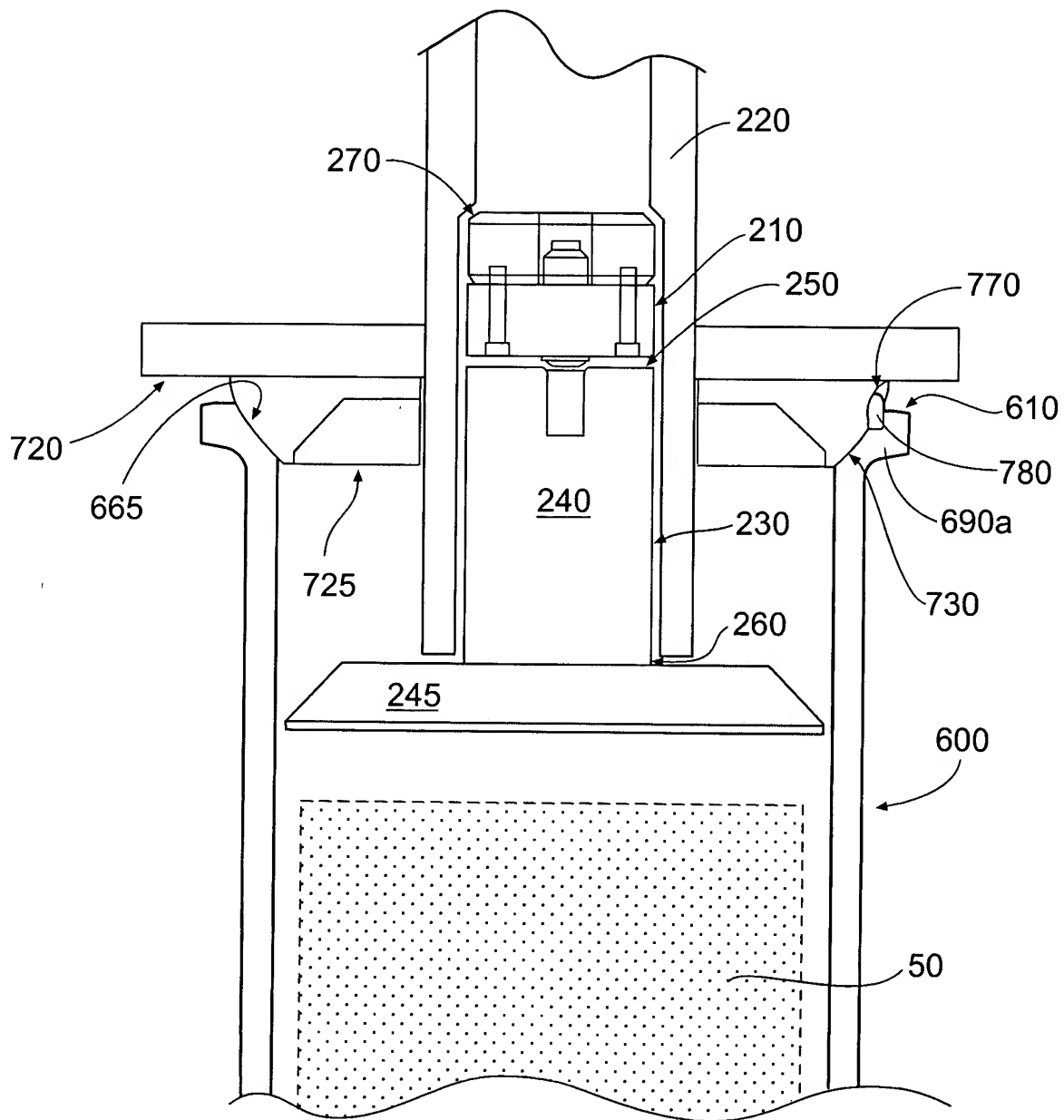
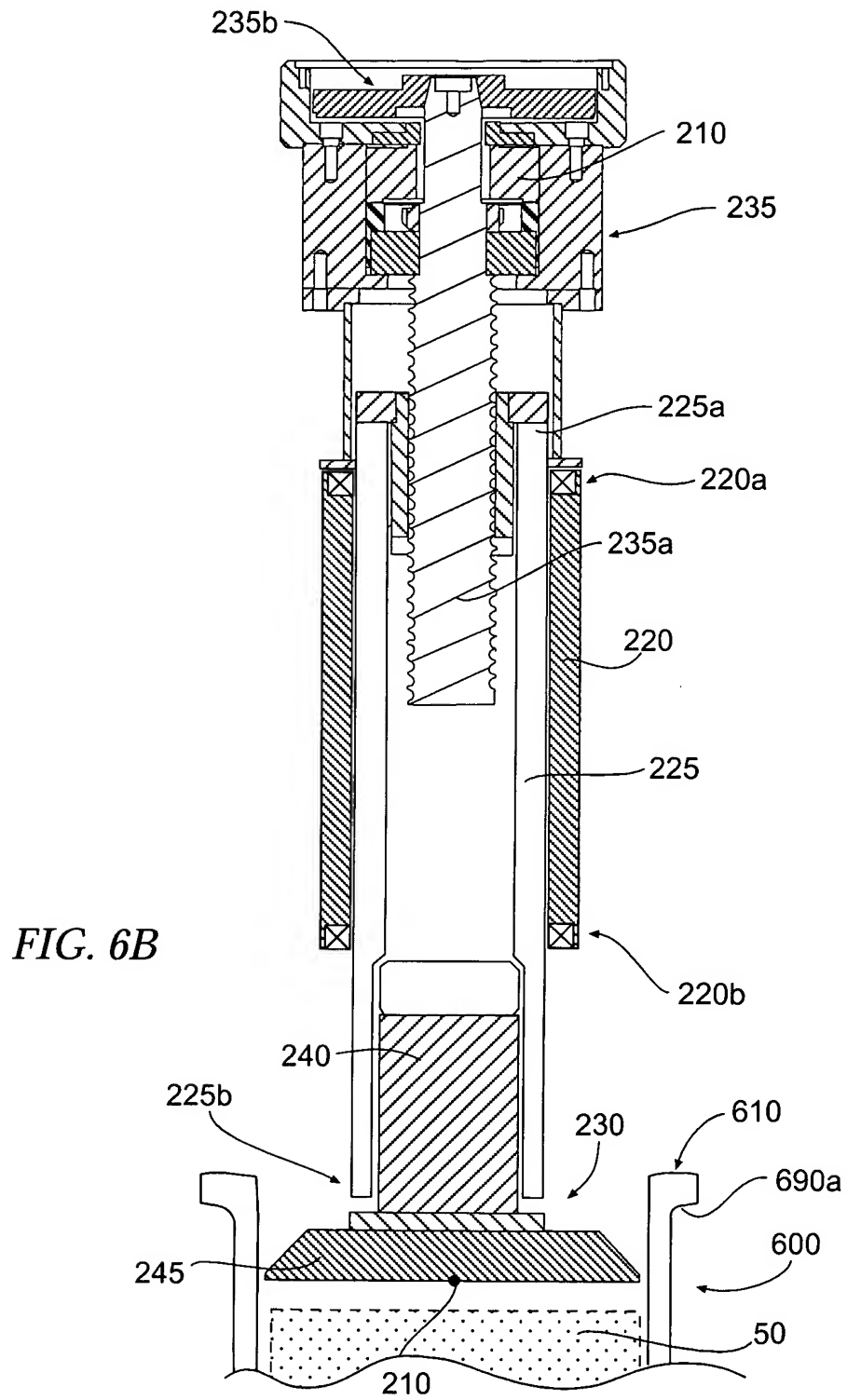
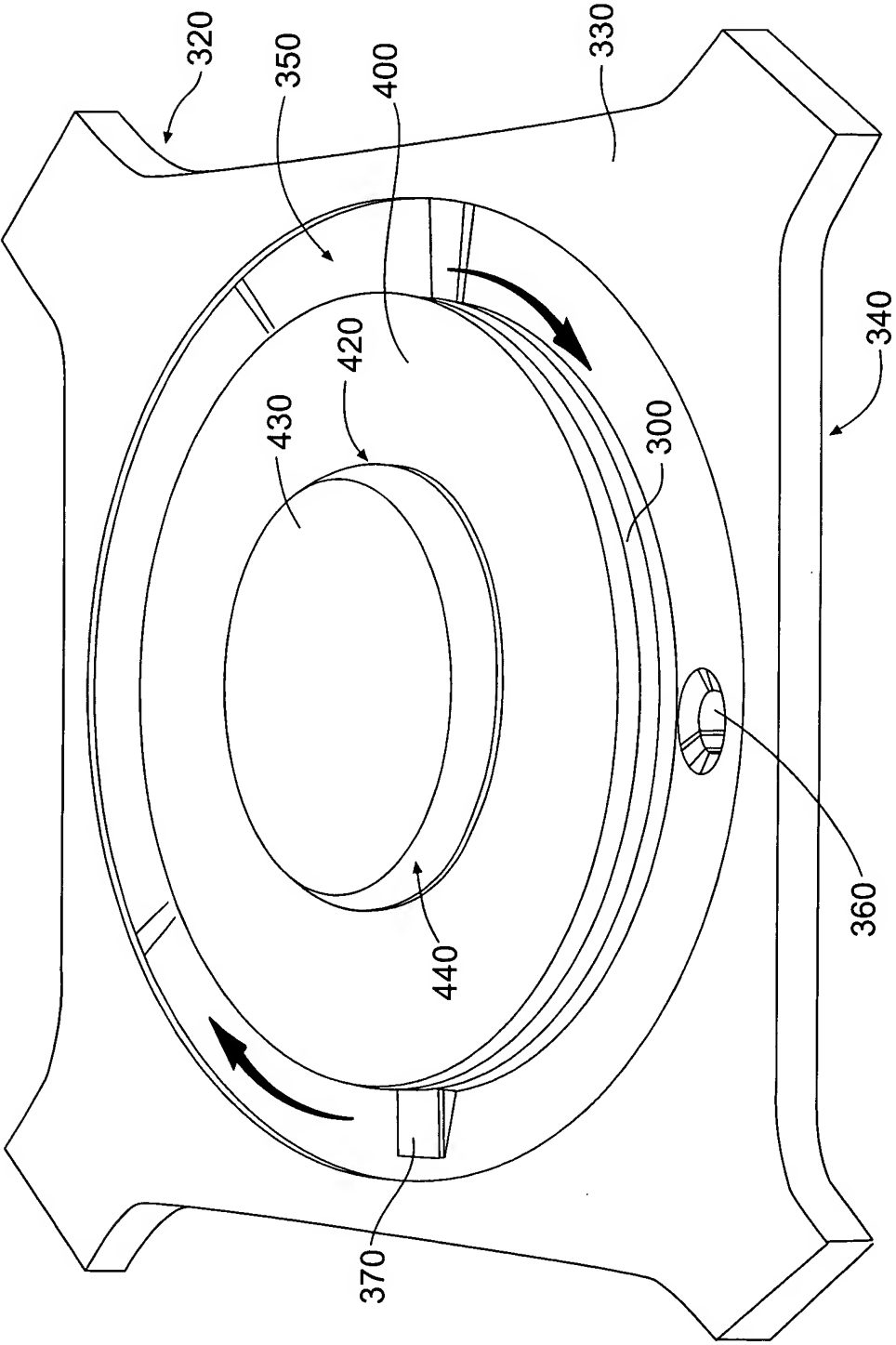


FIG. 6A

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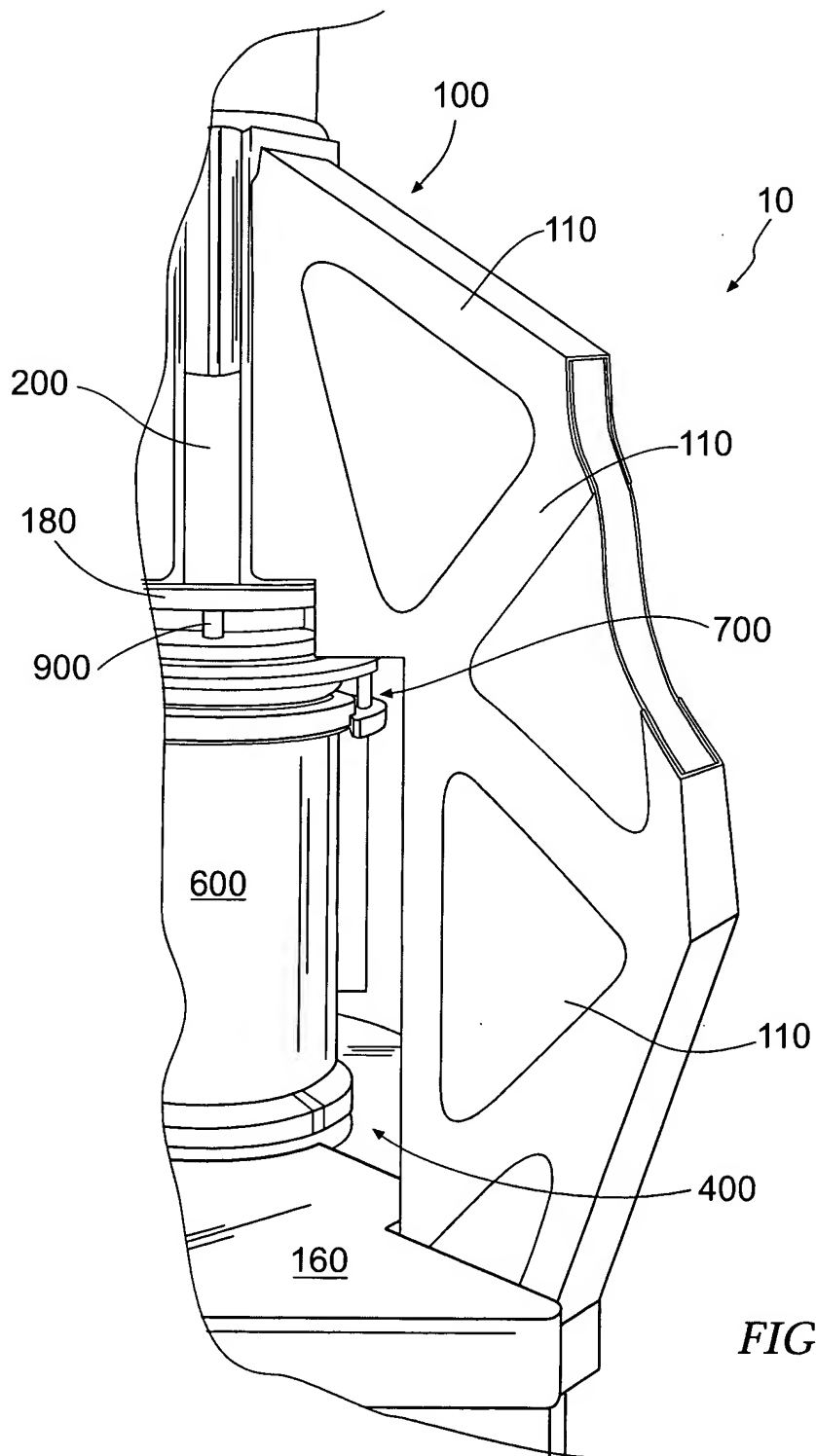


FIG. 8

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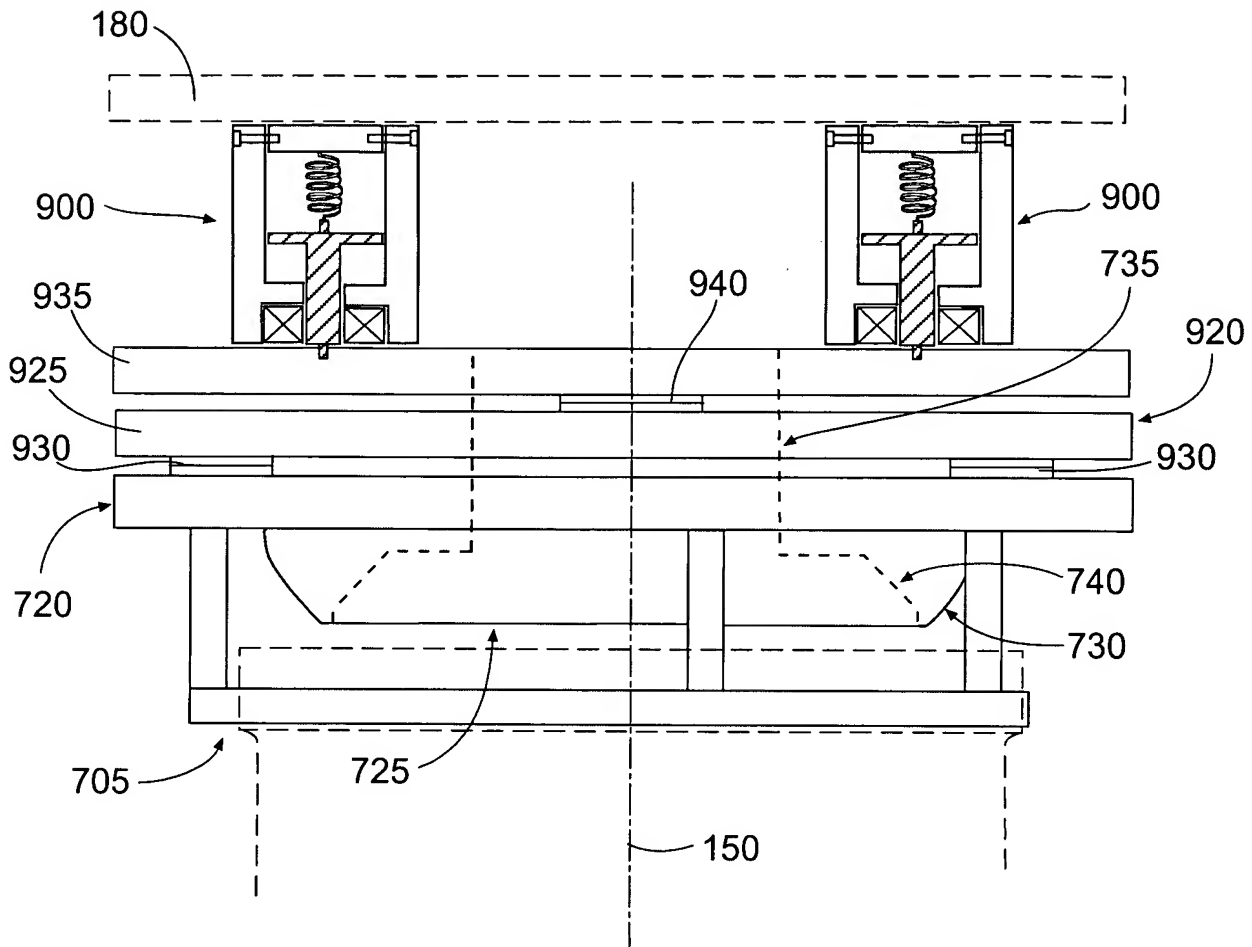


FIG. 9A

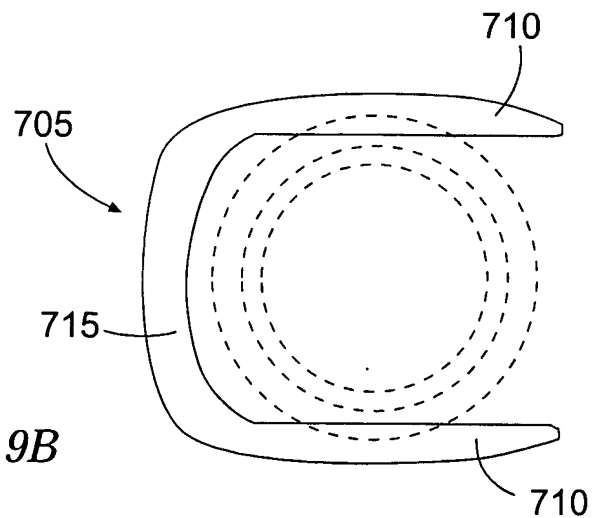
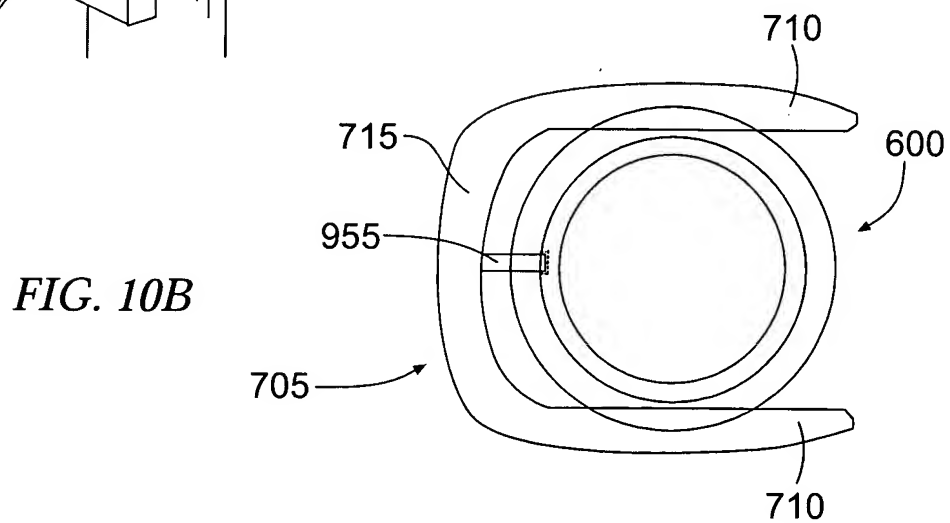
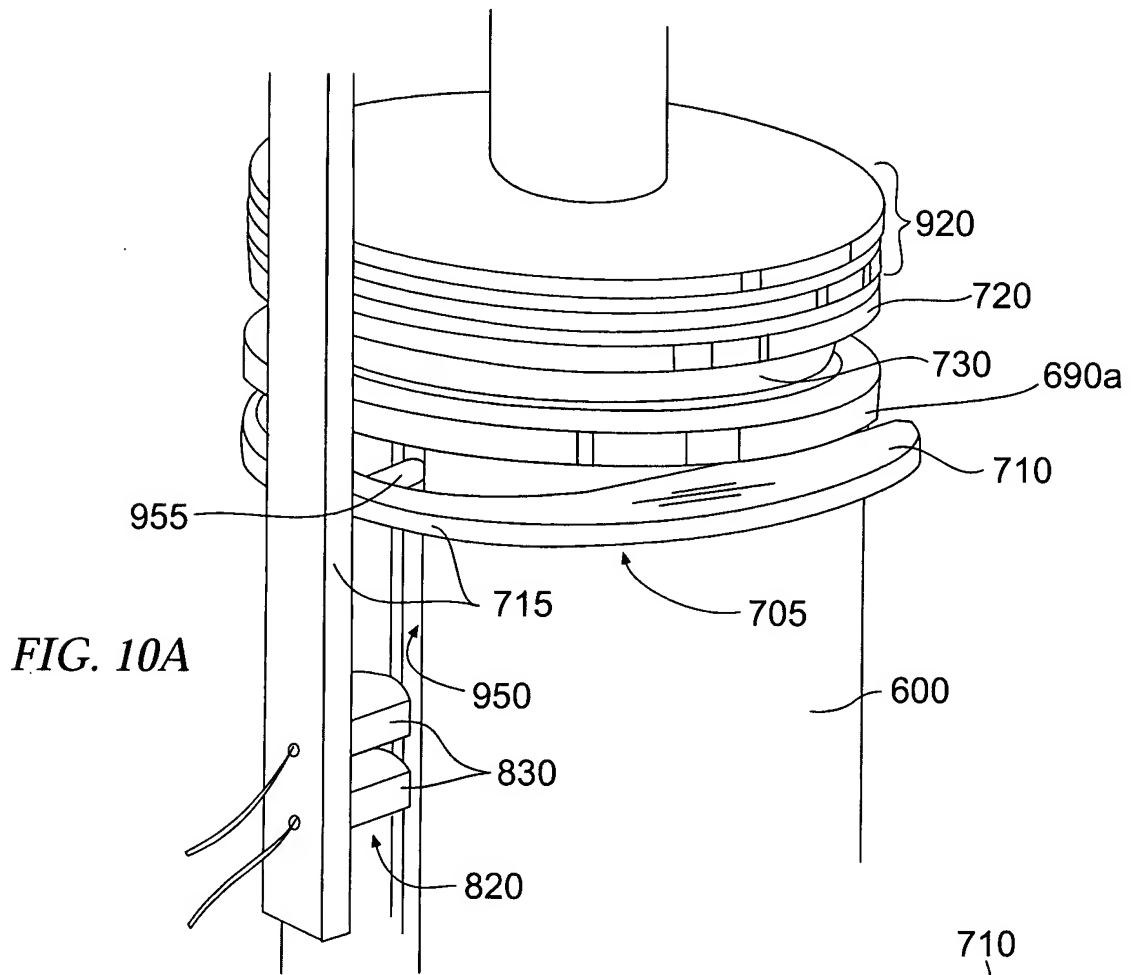


FIG. 9B

This cross-sectional view shows a semiconductor device assembly. A central substrate 300 is mounted on a base 320. A layer 400 is formed on the top surface of the substrate 300. A central region 420 is defined within layer 400. A vertical structure 600 is formed on the side surface of the central region 420. A layer 430 is formed on the top surface of the central region 420. A layer 660 is formed on the side surface of the vertical structure 600. A layer 630 is formed on the top surface of the layer 430. A layer 690b is formed on the side surface of the vertical structure 600. A layer 695a is formed on the top surface of the layer 690b. A layer 695b is formed on the side surface of the layer 695a. A layer 610 is formed on the top surface of the layer 695b. A layer 720 is formed on the top surface of the layer 610. A layer 200 is formed on the top surface of the layer 720. A layer 150 is formed on the top surface of the layer 200.

FIG. 9C

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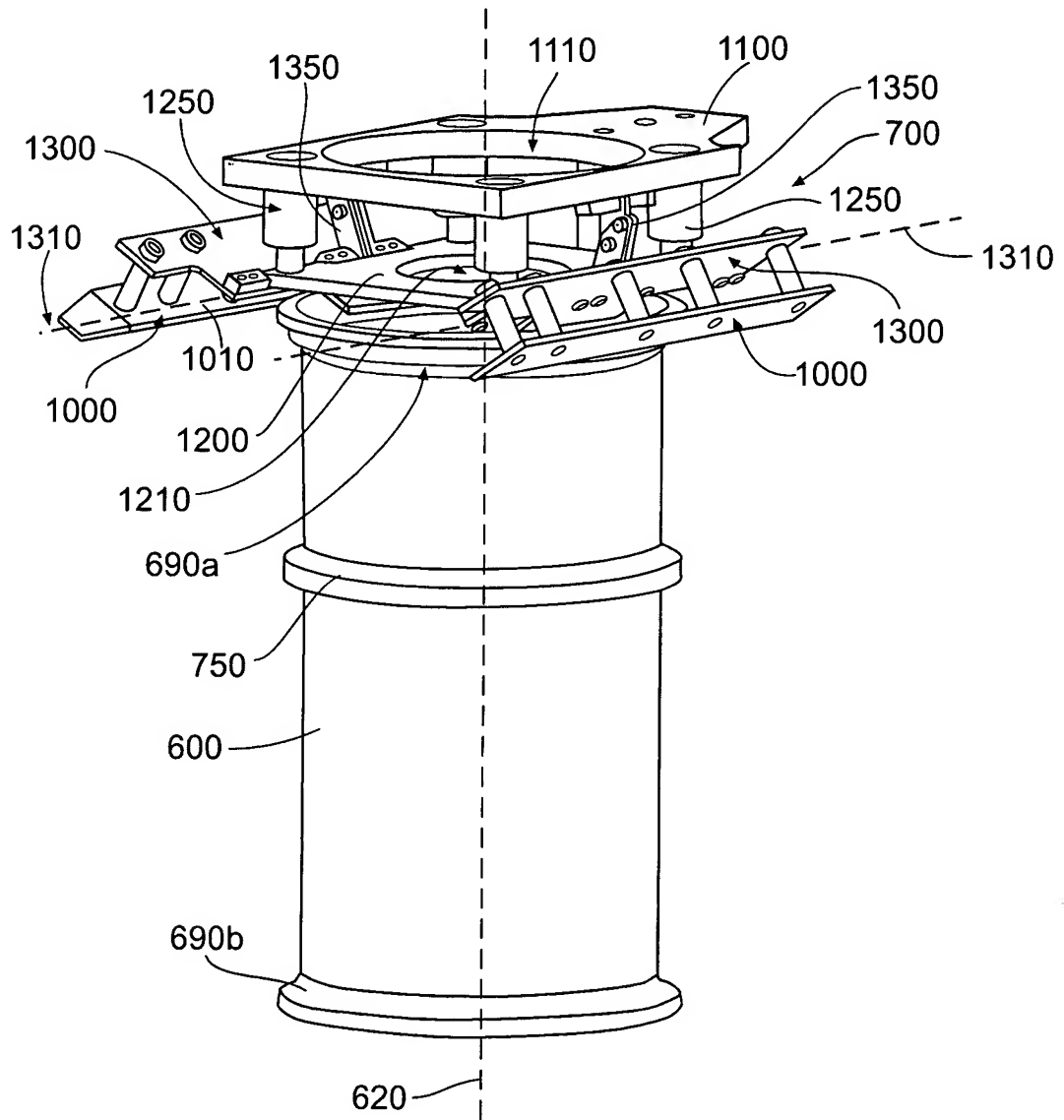
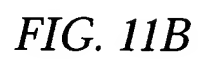


FIG. 11A



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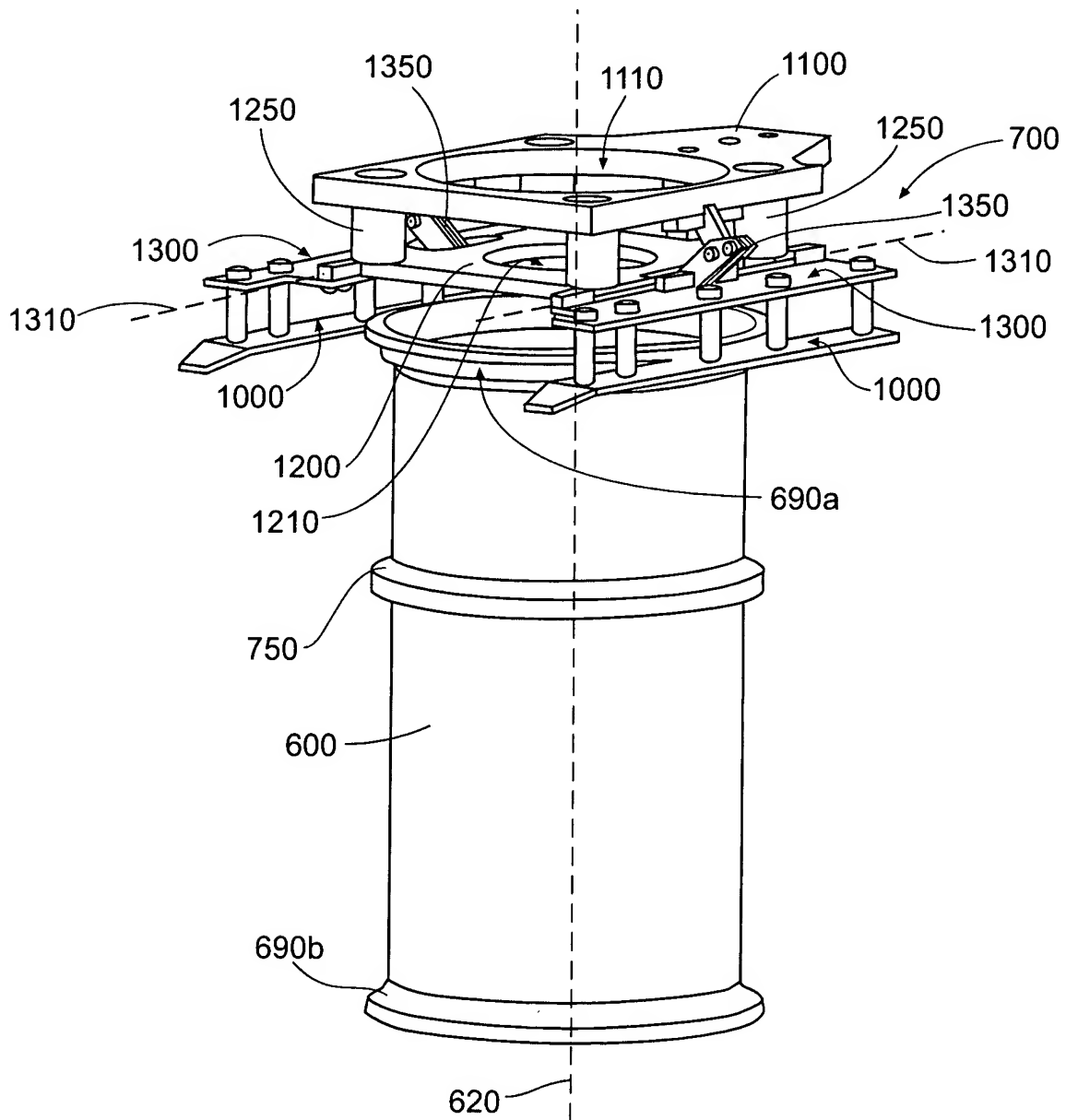


FIG. 11C

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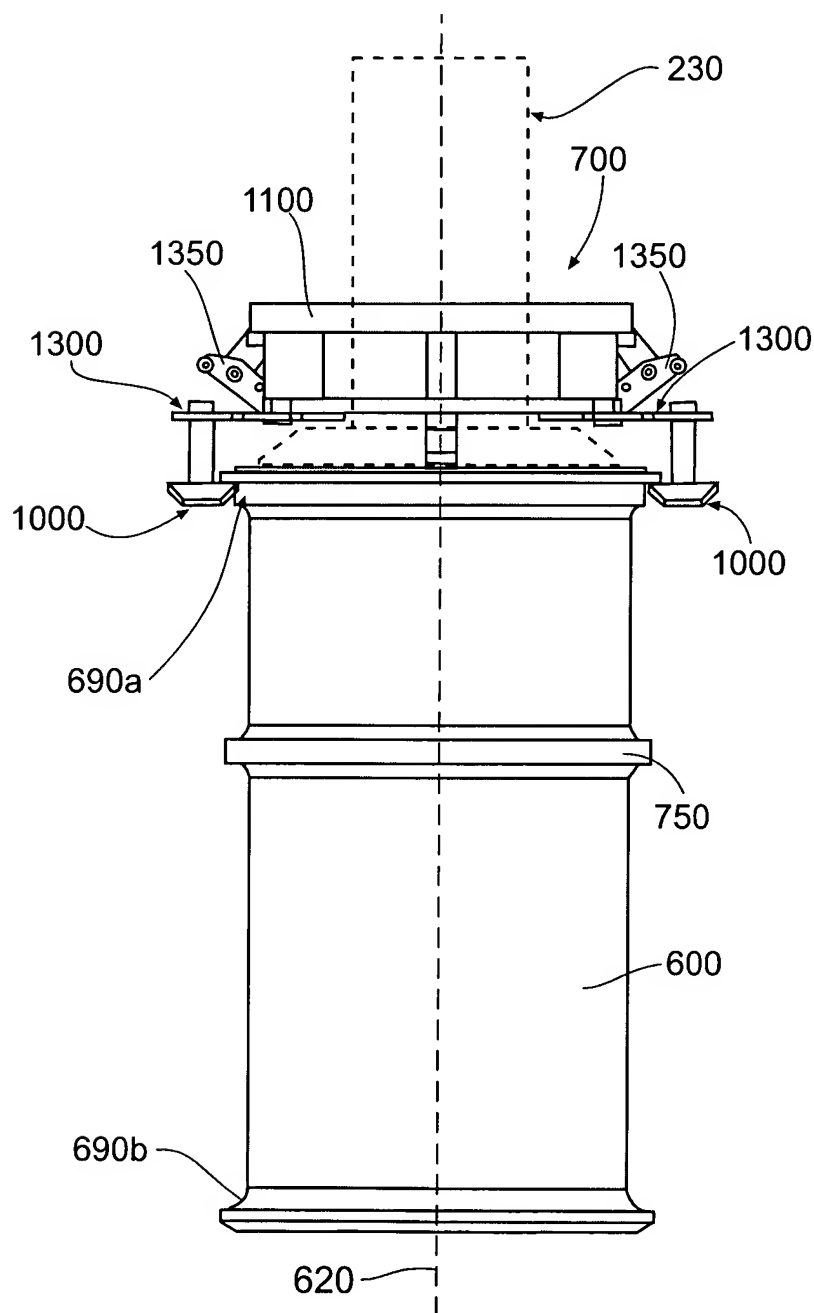


FIG. 11D

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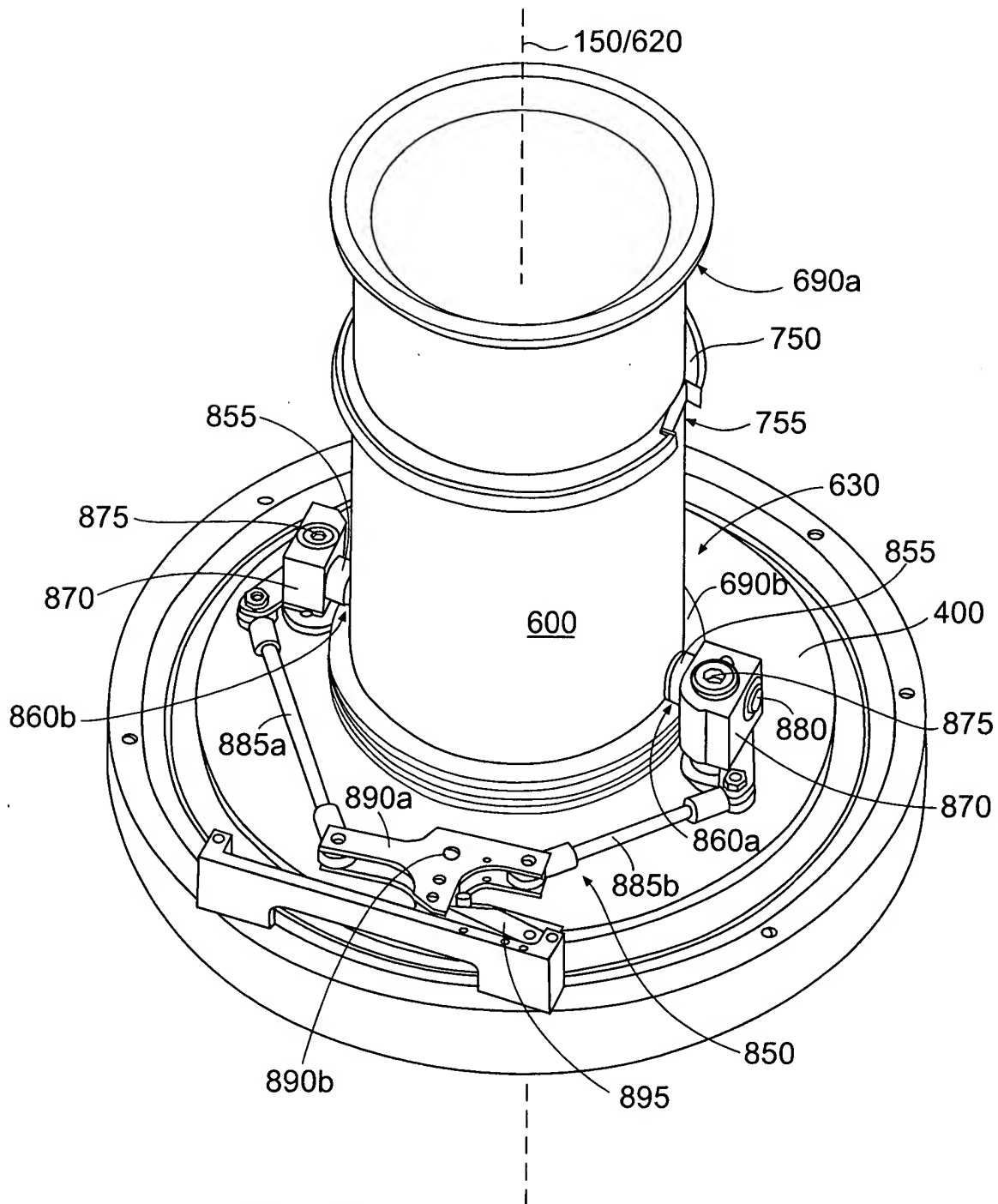
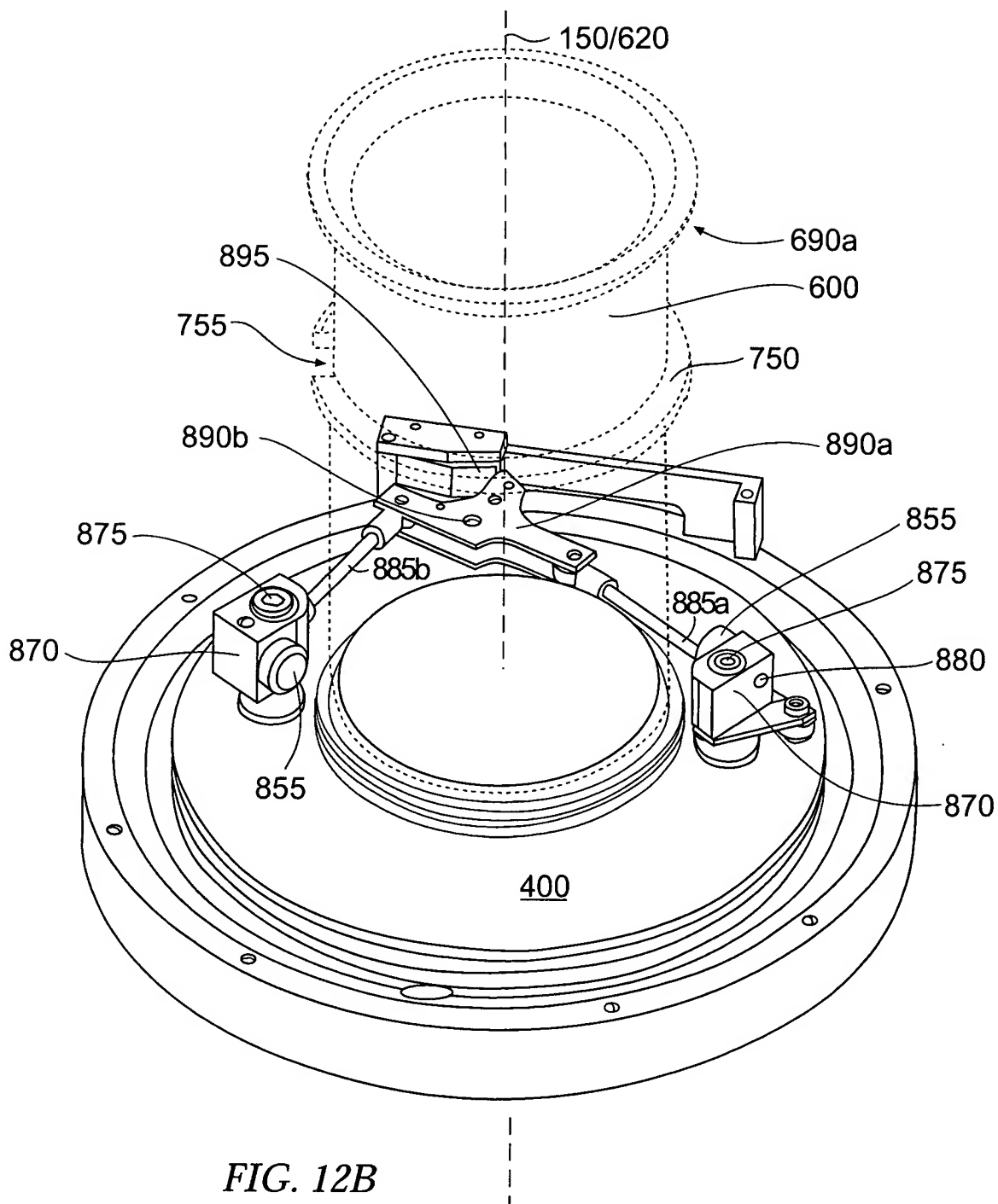


FIG. 12A

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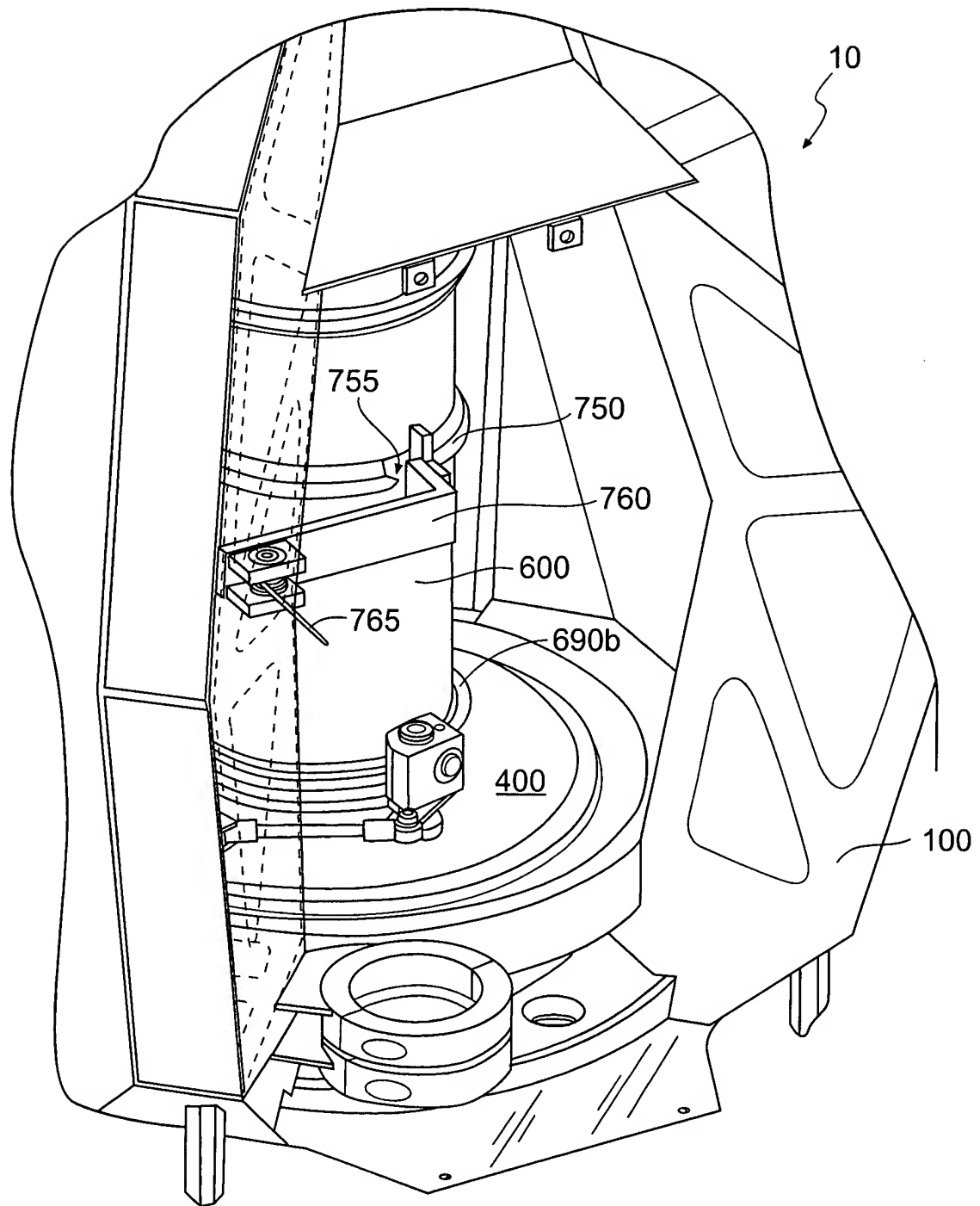
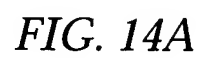


FIG. 13



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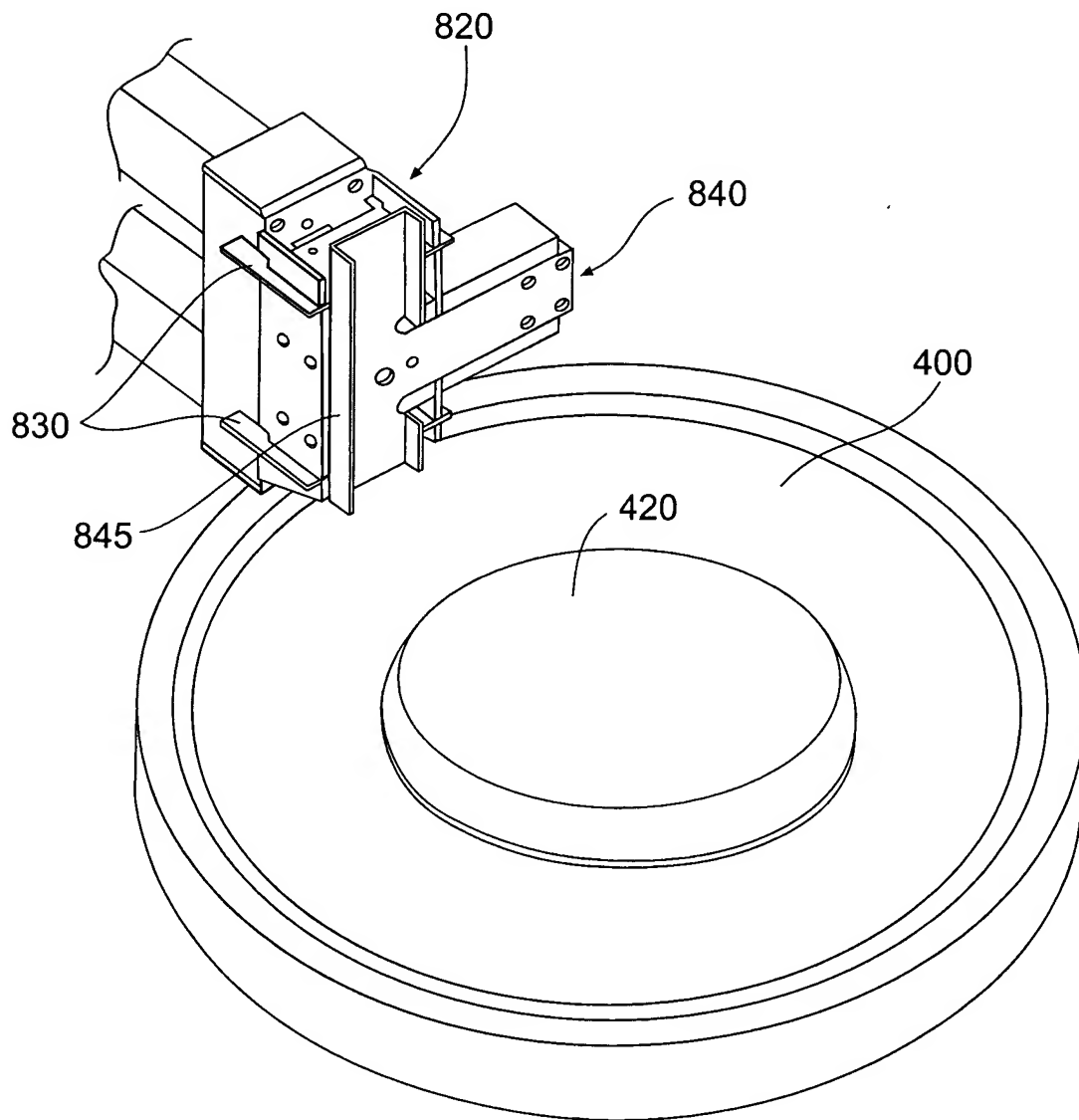


FIG. 14B